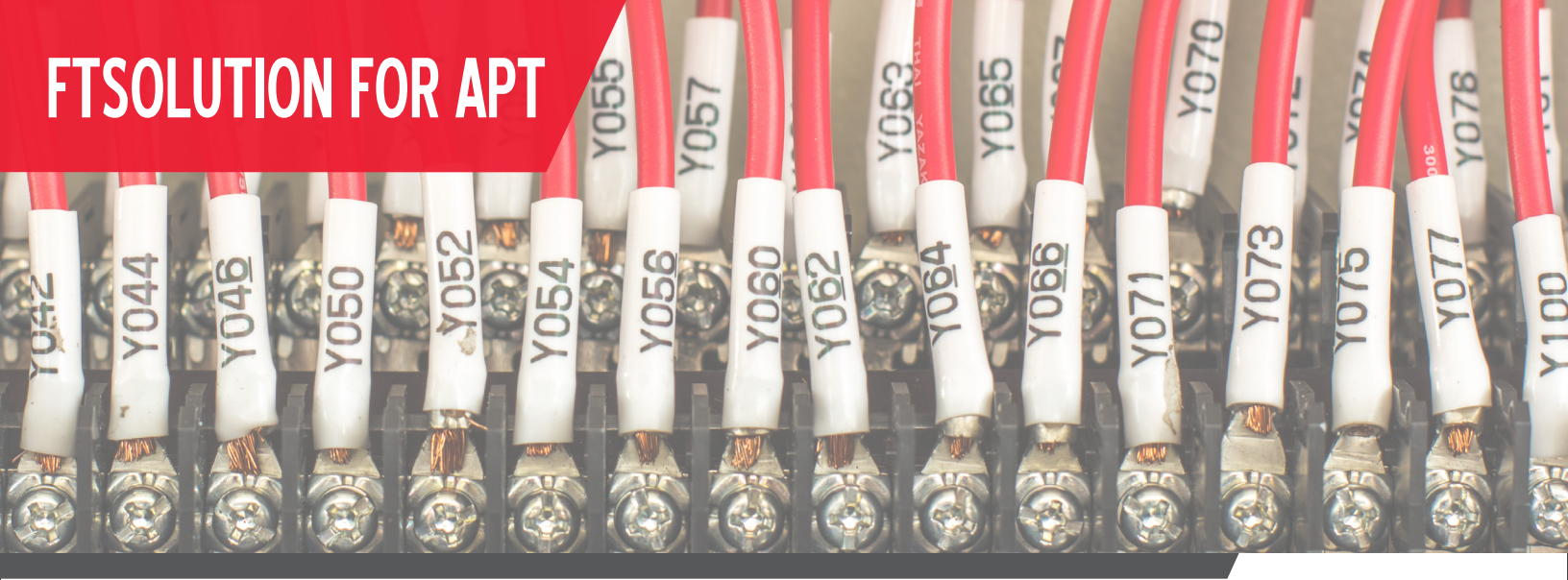# FTSOLUTION FOR APT

SOPHISTICATED PLC HARDWARE DEBUGGING

FTSolution for APT® Hardware Debugger provides a sophisticated PLC hardware debugger utilizing ladder language-based tools. FTSolution for APT Hardware Debugger helps you protect your PLC programs from unwanted changes by limiting functionality to only those tasks that need to be performed by the technicians who are responsible for maintaining the hardware systems running your APT programs.

Using FTSolution for APT Hardware Debugger, your technicians will be able to:

  • Download compiled FTSolution for APT programs into CTI 2500 Series and legacy 505 processors
  • Utilize sophisticated ladder language-based hardware debugging tools
  • Monitor data such as program data values, tags, descriptions, comments

FTSolution for APT Hardware Debugger provides:

  • A powerful hardware debugging environment
  • Protection against unwanted program changes
  • A low-cost solution for equipping your technicians with the tools to do their jobs

Supported PLCs
  • Control Technology - 2500 Series: C100; C200; C300; C400 (APT language optimized)
  • Legacy 505 PLCs:
      • 500 Series: **520** 1101; **520C** 1101, 1102; **530** 1102, 1104, 1108; **530C/T** 1104, 1108, 1112; **560** 2120; **565** 2120; **560T** 2820; **565T** 2820
      • 505 Series: **525** 1102, 1104, 1208; **535** 1204, 1212; **545** 1101–1106; **555** 1101–1106; **575** 2102–2106

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